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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10063472	FILING DATE 04/26/2002	CLASS 224	SUBCLASS 2654	GAU 2133 2858	EXAMINER J. KERVEROS
<b>**APPLICANTS:</b> Ozaki Hideharu; 714 2620 2133					
<b>**CONTINUING DATA VERIFIED:</b> none					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> Yes 1/29/04 JAPAN 2001-229629 07/30/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no Jung 1/29/04		ATTORNEY DOCKET NO 15483	
TITLE : Semiconductor integrated circuit device and device for testing same U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner	
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